

PCT

WORLD INTELLECTUAL PROPERTY ORGANIZATION
International Bureau

INTERNATIONAL APPLICATION PUBLISHED UNDER THE PATENT COOPERATION TREATY (PCT)

(51) International Patent Classification ⁶: B23K 26/00, G03F 7/42	A1	(11) International Publication Number: WO 97/17163 (43) International Publication Date: 15 May 1997 (15.05.97)
(21) International Application Number: PCT/IL96/00140 (22) International Filing Date: 4 November 1996 (04.11.96) (30) Priority Data: 115933 9 November 1995 (09.11.95) IL (71) Applicant (for all designated States except US): ORAMIR SEMICONDUCTOR EQUIPMENT LTD. [IL/IL]; P.O. Box 14, 31000 Haifa (IL). (72) Inventors; and (75) Inventors/Applicants (for US only): GENUT, Menachem [IL/IL]; 51 Leah Street, 34815 Haifa (IL). LIVSHITS (BUYANER), Boris [IL/IL]; 20/13 Yasmin Street, P.O. Box 6739, 21940 Carmiel (IL). TEHAR-ZAHAV, Ofer [IL/IL]; 28 Binyamin Avenue, 42311 Natania (IL). ISKEVITCH, Eliezer [IL/IL]; 1/11 Anilevitch Street, Neve-Ganim, 26000 Kiryat-Mozkin (IL). (74) Agents: LUZZATTO, Kfir et al.; Luzzatto & Luzzatto, P.O. Box 5352, 84152 Beer-Sheva (IL).		(81) Designated States: AL, AM, AT, AU, AZ, BA, BB, BG, BR, BY, CA, CH, CN, CU, CZ, DE, DK, EE, ES, FI, GB, GE, HU, IL, IS, JP, KE, KG, KP, KR, KZ, LC, LK, LR, LS, LT, LU, LV, MD, MG, MK, MN, MW, MX, NO, NZ, PL, PT, RO, RU, SD, SE, SG, SI, SK, TJ, TM, TR, TT, UA, UG, US, UZ, VN, ARIPO patent (KE, LS, MW, SD, SZ, UG), Eurasian patent (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European patent (AT, BE, CH, DE, DK, ES, FI, FR, GB, GR, IE, IT, LU, MC, NL, PT, SE), OAPI patent (BF, BJ, CF, CG, CI, CM, GA, GN, ML, MR, NE, SN, TD, TG). Published <i>With international search report.</i>
(54) Title: PROCESS AND APPARATUS FOR OBLIQUE BEAM REVOLUTION, FOR THE EFFECTIVE LASER STRIPPING OF SIDEWALLS (57) Abstract <p>A process and apparatus for carrying out a substantially complete and accelerated removal of coating substance from the sidewalls of a surface to be stripped, comprising providing a laser beam incidence on the surface at an incidence angle α relative to the plane of the surface being treated, the said angle $\alpha > 0$ and periodically switching or gradually changing the azimuth of beam incidence plane.</p> <div data-bbox="824 1155 1347 1879"> </div>		

FOR THE PURPOSES OF INFORMATION ONLY

Codes used to identify States party to the PCT on the front pages of pamphlets publishing international applications under the PCT.

AM	Armenia	GB	United Kingdom	MW	Malawi
AT	Austria	GE	Georgia	MX	Mexico
AU	Australia	GN	Guinea	NE	Niger
BB	Barbados	GR	Greece	NL	Netherlands
BE	Belgium	HU	Hungary	NO	Norway
BF	Burkina Faso	IE	Ireland	NZ	New Zealand
BG	Bulgaria	IT	Italy	PL	Poland
BJ	Benin	JP	Japan	PT	Portugal
BR	Brazil	KE	Kenya	RO	Romania
BY	Belarus	KG	Kyrgyzstan	RU	Russian Federation
CA	Canada	KP	Democratic People's Republic of Korea	SD	Sudan
CF	Central African Republic	KR	Republic of Korea	SE	Sweden
CG	Congo	KZ	Kazakhstan	SG	Singapore
CH	Switzerland	LI	Liechtenstein	SI	Slovenia
CI	Côte d'Ivoire	LK	Sri Lanka	SK	Slovakia
CM	Cameroon	LR	Liberia	SN	Senegal
CN	China	LT	Lithuania	SZ	Swaziland
CS	Czechoslovakia	LU	Luxembourg	TD	Chad
CZ	Czech Republic	LV	Latvia	TG	Togo
DE	Germany	MC	Monaco	TJ	Tajikistan
DK	Denmark	MD	Republic of Moldova	TT	Trinidad and Tobago
EE	Estonia	MG	Madagascar	UA	Ukraine
ES	Spain	ML	Mali	UG	Uganda
FI	Finland	MN	Mongolia	US	United States of America
FR	France	MR	Mauritania	UZ	Uzbekistan
GA	Gabon			VN	Viet Nam

PROCESS AND APPARATUS FOR OBLIQUE BEAM REVOLUTION,
FOR THE EFFECTIVE LASER STRIPPING OF SIDEWALLS

Field of the Invention

The present invention relates to U.V. laser surface treatment methods, particularly to the removal of foreign materials from substrate surfaces. More particularly, the invention relates to the use of oblique UV laser radiation beams for the removal of tough photoresist from semiconductor wafers by an improved dry laser stripping process.

Background of the Invention

In the manufacturing of various products it is necessary to apply a layer of protective material on a surface, which must be removed after a specified manufacturing step has been concluded. An example of such process is the so-called "masking", where a pattern is created on a surface using a layer of protective material illuminated through a mask, and the surface is then treated with a developer which removes material from the unmasked portions of the surface, therefore leaving a predetermined pattern. The surface is then treated by ion implantation or by etching agents, which introduce the implanted species into the unmasked portions of the surface, or removes material from unmasked portions. Once these processes are completed, the role of the protecting mask ends and it must be removed. The process is conventional and well known in the art, and is described, e.g., in U.S. Patent No. 5,114,834.

Two main photoresist stripping methods exist in the modern VLSI/ULSI (Very/Ultra Large Scale Integration) circuits industry:

-2-

- 1) Wet stripping which uses acids or organic solvents;
- 2) Dry stripping, which uses plasma, O_3 , O_3/N_2O or U.V./ O_3 -based stripping.

Both methods are problematic and far from being complete, especially when taking into consideration the future miniaturization in the VLSI/ULSI industry. The current technology is capable of dealing with devices having feature sizes of about $0.5\ \mu m$, but before the end of the century the expectation is that the workable size of the devices is expected to be reduced to $0.25\ \mu m$. The expected size changes require considerable changes in the manufacturing technology, particularly in the stripping stage. The prior art photoresist stripping techniques described above will be unsuitable for future devices, as explained hereinafter.

Utilizing only the wet stripping method is not a perfect solution, as it cannot completely strip photoresist after tough processes that change the chemical and physical properties of the photoresist in a way that it makes its removal very difficult. Such processes include, e.g., High Dose Implantation (HDI), Reactive Ion Etching (RIE), deep U.V. curing and high temperatures post-bake. After HDI or RIE the side walls of the implanted patterns or of the etched walls are the most difficult to remove.

In addition, the wet method has some other problems: the strength of stripping solution changes with time, the accumulated contamination in solution can be a source of particles which adversely affect the performance of the wafer, the corrosive and toxic content of stripping chemicals imposes high handling and disposal costs, and liquid phase surface tension and mass transport tend to make photoresist removal uneven and difficult.

The dry method also suffers from some major drawbacks, especially from metallic and particulate contamination, damage due to plasma: charges, currents, electric fields and plasma-induced U.V. radiation, as well as temperature-induced damage, and, especially, incomplete removal. During various fabrication stages, as discussed above, the photoresist suffers from chemical and physical changes which harden it, and this makes the stripping processes of the prior art extremely difficult to carry out. Usually a plurality of sequential steps, involving wet and dry processes are needed to remove completely the photoresist.

The art has addressed this problem in many ways, and commercial photoresist dry removal apparatus is available, which uses different technologies. For instance, UV ashers are sold, e.g. by Hitachi, Japan (UA-3150A), dry chemical ashers are also available, e.g., by Fusion Semiconductor Systems, U.S.A., which utilize nitrous oxide and ozone to remove the photoresist by chemical ashing, microwave plasma ashing is also effected, e.g., as in the UNA-200 Asher (ULVAC Japan Ltd.). Also plasma photoresist removal is employed and is commercially available, e.g., as in the Aspen apparatus (Mattson Technology, U.S.A.), and in the AURA 200 (GASONICS IPC, U.S.A.).

More recently, photoresist removal has been achieved by ablation, using laser UV radiation, in an oxidizing environment, as described in U.S. Patent No. 5,114,834. The ablation process is caused by strong absorption of the laser pulse energy by the photoresist. The process is a localized ejection of the photoresist layer to the ambient gas, associated with a blast wave due to chemical bonds breaking in the photoresist and instant heating. The partly gasified and partly fragmented photoresist is blown upwards away from the

surface, and instantly heats the ambient gas. Fast combustion of the ablation products occurs, due to the blast wave and may also be due to the photochemical reaction of the UV laser radiation and the process gases. The main essence of the process is laser ablation with combustion of the ablated photoresist which occurs in a reactive gas flowing through an irradiation zone. The combination of laser radiation and fast combustion provides instantaneous lowering of the ablation threshold of hard parts of the photoresist (side walls). The combusted ablation products are then removed by vacuum suction, or by gas sweeping leaving a completely clean surface.

While US 5,114,834 provides an important novel process, it still does not provide a high throughput, which is industrially convenient, viz., an industrially acceptable number of wafers that can be stripped during a given time. The laser stripping throughput is determined by the stripping rate or by the number of laser pulses needed for providing complete stripping of a unit area of the photoresist per unit of time.

While reference will be made throughout this specification to the ablation of photoresist from semiconductor wafers, this will be done for the sake of simplicity, and because it represents a well known and widely approached problem. It should be understood, however, that the invention described hereinafter is by no means limited to the stripping of photoresist from wafers, but it applies, *mutatis mutandis*, to many other applications, such as stripping and cleaning of photoresist from Flat Panel Displays (FPD) or removal of residues from different objects, such as lenses, semiconductor wafers, or photo-masks.

In the process of microlithography, the toughest structures, subjected to stripping together with photoresist, are the films forming on the sidewalls of VLSI/ULSI patterns. However, the art has so far failed to provide suitable means for effectively and completely remove such tough sidewall films in a dry process.

Summary of the Invention

The process for the complete and accelerated removal of photoresist from a wafer surface (including sidewalls to be stripped) according to the invention, comprises providing a laser beam oblique incidence angle $\alpha > 0$ relatively normal to the plane of the substrate being treated, and periodically (during the scanning of a laser beam footprint on a wafer), switching the laser beam azimuth, that is, effecting an oblique beam revolution.

Brief Description of the Drawings

In the drawings:

- Fig. 1 schematically illustrates the limitation on a beam incidence angle α ;
- Fig. 2 (a and b) summarizes experimental results for the stripping degree of sidewalls films on Al as a function of the incidence angle α and the number of pulses; a) is a two-dimensional representation of the results, and b) is a corresponding three-dimensional representation;
- Fig. 3 schematically illustrates the discrete optical switching of the laser beam azimuth ϕ , according to one embodiment of the invention;
- Fig. 4 schematically illustrates the discrete optical switching of the laser beam azimuth ϕ according to another embodiment of the invention;
- Fig. 5 schematically shows the continuous switching of the laser beam azimuth ϕ according to still another embodiment of the invention;

Fig. 6 shows experimental results which illustrate the role of laser beam azimuthal orientation for the complete stripping of sidewall films.

Detailed Description of the Invention

The magnitude of the angle α , of course, depends on the topography of VLSI/ULSI circuits on a wafer, as is schematically illustrated in Fig. 1. Since, as will be more fully showed below, the stripping rate of sidewall films increases with increasing beam incidence angle α (Fig. 2), according to a preferred embodiment of the invention, in which it is desired to maximize the stripping rate, the said angle α is selected according to the following rule:

$$\alpha = \alpha_{\max} = \text{arcctg} (AR)_{\max}$$

wherein $(AR)_{\max}$ is the maximum aspect ratio for a given type of wafer. The aspect ratio is defined as the ratio h/w of the height of a metal protrusion to the distance between two adjacent protrusions, as more fully explained below with reference to Fig. 1.

Laser stripping of sidewalls using the angle α_{\max} as the incidence angle shortens processing time, compared to the result obtained when operating with a laser beam normal to the wafer surface. This result as the direct consequence of the phenomenon illustrated in Fig. 2, can exceed two times.

As will be appreciated by the skilled person, it is desirable to provide for the purpose of the invention apparatus having an optics line of laser stripper which is capable of providing a wide range of values of the angle α , to permit to carry out both bulk and sidewall stripping and cleaning operations, which require different incidence angles, using the same apparatus.

-7-

Switching the laser beam azimuth may be implemented in a discrete manner, as well as continuously. In the case of a limited number of different orientations for sidewalls, the switching may be discrete, as schematically illustrated in Figs. 3 and 4. If the contours change continuously, as in round walls, or where a great number of sidewalls orientations is involved, the switching is preferably carried out in a continuous manner, as schematically shown in Fig. 5.

The switching operation may be realized either optically, by changing the azimuth of the laser beam, as schematically illustrated in Fig. 3, or mechanically, as illustrated in Fig. 4, by a wafer revolution.

The abovementioned effects concerning sidewall film stripping are illustrated in Fig. 6, showing actual stripping results experimental data. The photographs shown in Fig. 6 show the remaining sidewall film after successive applications of laser beam irradiation with one azimuth only. One can clearly see that the remaining sidewalls are parallel to the incidence beam. In order to remove completely these side walls one should switch the beam azimuth by $\Delta\phi=90^\circ$.

Detailed Description of the Drawings

The incidence angles discussed herein are defined with reference to Figs. 1 and 2. Fig. 1 schematically shows a cross-section of a wafer portion, generally indicated by numeral 1, containing a topographical element with a maximum aspect ratio (defined below). Protrusions 2 and 2' of metals on the substrate are covered by sidewall films 3, having arisen as a result of metal etching during a microlithography process. The angle α is defined as the angle between the laser beam and the normal to the treated surface. The three possible directions

of the laser beam are schematically indicated above the topographical element, by L^1 ($\alpha = \alpha_{\max}$), L^2 ($\alpha > \alpha_{\max}$) and L^3 ($\alpha < \alpha_{\max}$). At the incidence angle $\alpha < \alpha_{\max}$, as is clearly seen in Fig. 1, the sidewall film is fully illuminated by the laser beam and, at the incidence angles $\alpha > \alpha_{\max}$, the upper part of the film only is illuminated.

As can be seen from Fig. 1, the value of the incidence angle α_{\max} is associated with a maximum value of aspect ratio $AR=h/w$, where "h" is the height of metal protrusion and "w" is the distance between two adjacent protrusions.

As stated, Fig. 2 (a and b) summarizes experimental results for the stripping degree of sidewalls films on Al (hereinafter the response) as a function of the incidence angle α and the number of pulses; a) is a two-dimensional representation of the results, and b) is a corresponding three-dimensional representation. In both figures 2a and 2b, the X-axis and the Y-axis are the angle axis and the number of pulses axis, respectively, and the additional Z-axis in the three-dimensional representation is the response, said response having values between 0 to 3, with 3 indicating complete removal and 0 no removal at all. Since, as is seen from the experimental data shown in Fig. 2, the laser stripping rate of sidewall film increases when the incidence angle α increases, the optical line of the laser stripper should provide an oblique beam incidence at the angle:

$$\alpha \leq \alpha_{\max} = \text{arcctg} (h/w)_{\max}.$$

As will be appreciated by the skilled person, additional stripping is required for the removal of the right sidewall film. For this purpose, one needs to alternate the azimuth of laser beam represented in Fig. 1 by an angle $\phi=180^\circ$.

Figs. 3 and 4 illustrate two possible ways of achieving the switching of laser beam azimuth by an angle $\varphi=180^\circ$, according to two possible alternative embodiments of the invention, namely, by optical and mechanical means.

Fig. 3 illustrates the principle of optical switching for laser beam azimuth during the scanning of its footprint on a wafer. A wafer 4 is located on a chuck 5, and a scanning head 6, from which the laser beam reaches the wafer 5, moves relatively to the wafer, in a scanning movement. The scanning head 6 comprises, in the particular embodiment schematically illustrated in this figure, a rotating mirror 7, and two fixed mirrors 8 and 8'. Rotating mirror 7 moves from a first position, S-S', to a second position T-T', with a frequency f . A laser beam L hits the surface of rotating mirror 7 and, when the latter is in the T-T' position, is transferred to inclined fixed mirror 8, reaching the wafer surface from it with a first azimuth, while when the rotating mirror 7 is in the S-S' position, the beam reaches inclined fixed mirror 8', thus hitting the wafer surface with an azimuth changed by 180° . According to the scheme shown in Fig. 3, the scanning of the wafer proceeds at a speed v , and the laser beam L hits the surface of the wafer with a pulse repetition rate f (pps) at a point P, the angle of incidence of the beam being α in the incidence plane OMP. In the time interval $\Delta t = 1/f$, the beam azimuth alternates by the angle $\Delta\varphi=180^\circ$, by means of an appropriate revolution of mirror as explained above. This procedure and apparatus are most conveniently used when fast switching is required, although they can be exploited also for relatively slow changes, by suitable adjustments which are apparent to the skilled person. The same optical scheme can be used, e.g., if slow switching is required, when the azimuth change takes place only after the scanning of all the wafer has been completed, that is, on the edges of the wafer only. Then the switching of the

azimuth φ can be performed in any time interval $\Delta t = D/v$, where D is the diameter of the wafer and v is the scanning velocity.

Fig. 4 illustrates the situation in which slow discrete switching is effected by mechanical means ($\Delta\varphi=180^\circ$), which can be achieved through a revolution of the wafer 9 by an angle $\Delta\varphi=180^\circ$ every time the laser beam footprint reaches the wafer edge. A small square element, with corners identified by A-B-C-D is shown at the incidence point of the laser beam, to illustrate the rotation by $\Delta\varphi=180^\circ$ between Fig. 4a and Fig. 4b. The angle α between the laser beam L and the normal plane N to the wafer, is also shown.

Fig. 5 illustrates the continuous relative revolution of the azimuthal plane of the laser beam L and the wafer 10. Here, a wafer revolution is shown with the angular velocity

$$\omega = \frac{360^\circ}{(D/v)n}$$

where n is the number of passages of wafer by laser beam footprint needed for a complete stripping of all the sidewalls films A, B, C, etc., schematically shown in Fig. 5 to represent walls to be continuously stripped.

Fig. 6 depicts the experimental results for the sidewalls films stripping with a laser beam incidence plane perpendicular to one of the two orientations of sidewalls on the wafer. These pictures were taken on the Optical (figures 6a and 6c) and Scanning Electron Microscopes (figures 6b, 6d and 6e) for 50 and 100 pulses. Figures 6a and 6b refer to the 50 pulses, while Figures 6c, 6d and 6e refer to 100 pulses. Numerals 1 and 2 indicate the laser beam direction. They show the insufficiency of one laser beam azimuthal orientation for

complete stripping when there are two different rectangular orientations of sidewalls on a wafer surface.

All the above and other characteristics and advantages of the invention will be better understood through the following illustrative and non-limitative examples.

Example 1

A 6" wafer after metal deposition was covered with a layer of Novolak type positive photoresist, 1.2 μm thick, patterned by the conventional procedure described above, and then was subjected to RIE. During the RIE process, the metallic sidewalls, as well as the photoresist, were covered with a metalloorganic polymeric film forming on the so-called sidewalls.

Following the RIE, the wafer was treated in a process chamber consisting of a base and a cover, which are connected by air-tight connections so that the inner part of the process chamber defined by the said base and cover can be kept under pressure or vacuum. The base is provided with N_xO_y gas inlet, and O_2/O_3 inlet, and gas outlet for exhausting gases which have passed through the irradiated zone. The base is further provided with a chuck, on which the element to be stripped, e.g., a wafer, is supported. Through the center of chuck vacuum is applied, to hold the wafer in place during the process. A fused silica window is provided above the element to be stripped, e.g., a silicon wafer. This window permits the passage of the laser beam which originates from a source positioned above the chamber. A silica cover frame keeps the silica windows in place, and assists in keeping the chamber pressurized or under vacuum.

Such a chamber is described and claimed in a copending Israeli patent application No. 115934 of the same applicant, the description of which is incorporated herein by reference.

The process was operated according to U.S. Patent No. 5,114,835, using ozone as the reactive gas. The pressure in the chamber was maintained at 400 mbar. Laser radiation was employed in which the incident angle was varied in the range of 5° to 45° in respect to the normal wafer plane. The laser employed was a Lambda Physik LPX 315 IMC excimer laser. The results of the experiments subjected to statistical software are shown when 2a of Fig. 2 is in 2D form and 2b is in 3D form. The plots show the decrease of the number of pulses needed for stripping as a function of increasing the incidence angle of illumination.

As explained above, the values between 0 to 3 are the response, and indicate the degree of sidewall film removal, with 3 indicating complete removal and 0 no removal at all. (S.W. Al indicates sidewalls on aluminum). The improvement while illuminating with 45° in respect to 5° can be seen when referring to response 2.750. One can see that at 5° , 50 pulses are needed. However, when illuminating with 45° , only 20 pulses are needed for the same quality of removal. This shows that the throughput for stripping is more than doubled when increasing the incidence angle.

When referring to response 3.000 which, as said, means complete removal, one can see that the most dramatic improvement is observed in the range of $10-30^\circ$. The complete removal was verified by several analytical techniques, such as Auger Electron Spectroscopy (AES), Scanning Electron Microscopy (SEM).

Total Reflection X-Ray Fluorescence (TXRF) and Atomic Absorption Spectroscopy (AAS).

Example 2

A 6" wafer covered with a layer of patterned Novolak type photoresist (high dose implanted, 1.2 μm thick; the implantation dose was 5×10^{15} atoms/cm² and energy of 80 Kev) was treated in a process chamber with laser radiation, as described in Example 1. The effect of the azimuth of incidence plane of laser beam is clearly seen in Fig. 6, where optical and SEM micrographs of the pattern are shown. One can clearly see the preferential etching of the sidewalls perpendicular to the beam incidence plane compared to the sidewalls parallel to the laser beam. In order to complete the stripping process in reduced time, the azimuth of the laser beam plane should be changed by 90° (in the wafer's plane).

As can be seen by comparing the side walls of Figs. 6a and 6b, with those of Figs. 6c and 6d, respectively, without switching the beam azimuth, even when using a double number of pulses, the side walls are not entirely removed (Fig. 6d). On the other hand, 100 pulses were sufficient to remove all side walls in the direction of stripping perpendicular to the incidence beam (Fig. 6d). Therefore, in order to remove all remaining side walls of Fig. 6d, it is necessary to switch the azimuth of the beam, according to the present invention. If the azimuth is not changed, experiments show that the number of pulses needed for complete sidewall removal is considerably more than when the azimuth is changed.

Fig. 6e shows in greater detail one of the remaining side walls shown in Fig. 6d.

All the above description and examples have been given for the purpose of illustration, and are not intended to limit the invention in many ways. Many modifications can be carried out in the various means for changing the azimuth of the laser beam, different angles can be employed, and different optical and mechanical apparatus can be used, all without exceeding the scope of the invention.

CLAIMS:

1. A process for carrying out a substantially complete and accelerated removal of coating substance from the sidewalls of a surface to be stripped, comprising providing a laser beam incidence on the surface at an incidence angle α relative to the plane of the surface being treated, the said angle $\alpha > 0$ and periodically switching or gradually changing the azimuth of beam incidence plane.
2. A process according to claim 1, wherein the coating substance comprises photoresist or elementoorganic and inorganic foreign materials.
3. A process according to claim 1, wherein the magnitude of the angle α is $\alpha < \alpha_{\max} = \arctg (AR)_{\max}$, where AR is the aspect ratio of the topographical elements the sidewalls of which are being stripped.
4. A process according to any one of claims 1 to 3, wherein the beam azimuth is switched in a discrete manner.
5. A process according to any one of claims 1 to 3, wherein the beam azimuth is switched in a continuous manner.
6. A process according to any one of claims 1 to 4, wherein the frequency of beam azimuth switching equals the pulse repetition rate f.
7. A process according to any one of claims 1, 3 or 5, wherein the switching is carried out by oblique beam revolution with angular velocity $\omega = \frac{360^\circ}{(D/v)n}$.

8. Apparatus for the removal of coating substance from the sidewalls of a surface to be stripped, comprising providing a laser source to generate a beam, means for causing the beam to impinge on said surface at an angle α relative to the plane of the surface being treated, the said angle $\alpha > 0$, and means for periodically switching or gradually changing the azimuth of the beam incidence plane.

9. Apparatus according to claim 8, wherein the means for causing the beam to impinge on said surface comprise one or more optical components.

10. Apparatus according to claim 8 or 9, wherein the means for causing the azimuth of the beam incidence plane to change comprise a rotating optical component.

11. Apparatus according to claim 9 or 10, wherein the optical components include mirrors.

12. Apparatus according to claim 8, 9 or 11, wherein the means for causing the azimuth of the beam incidence plane to change comprise means for rotating the surface being treated.

-17-

13. A process for carrying out a substantially complete and accelerated removal of coating substance from the sidewalls of a surface to be stripped, substantially as described and with particular reference to the examples.

14. Apparatus for the removal of coating substance from the sidewalls of a surface to be stripped, substantially as described and with particular reference to the examples.

1/8

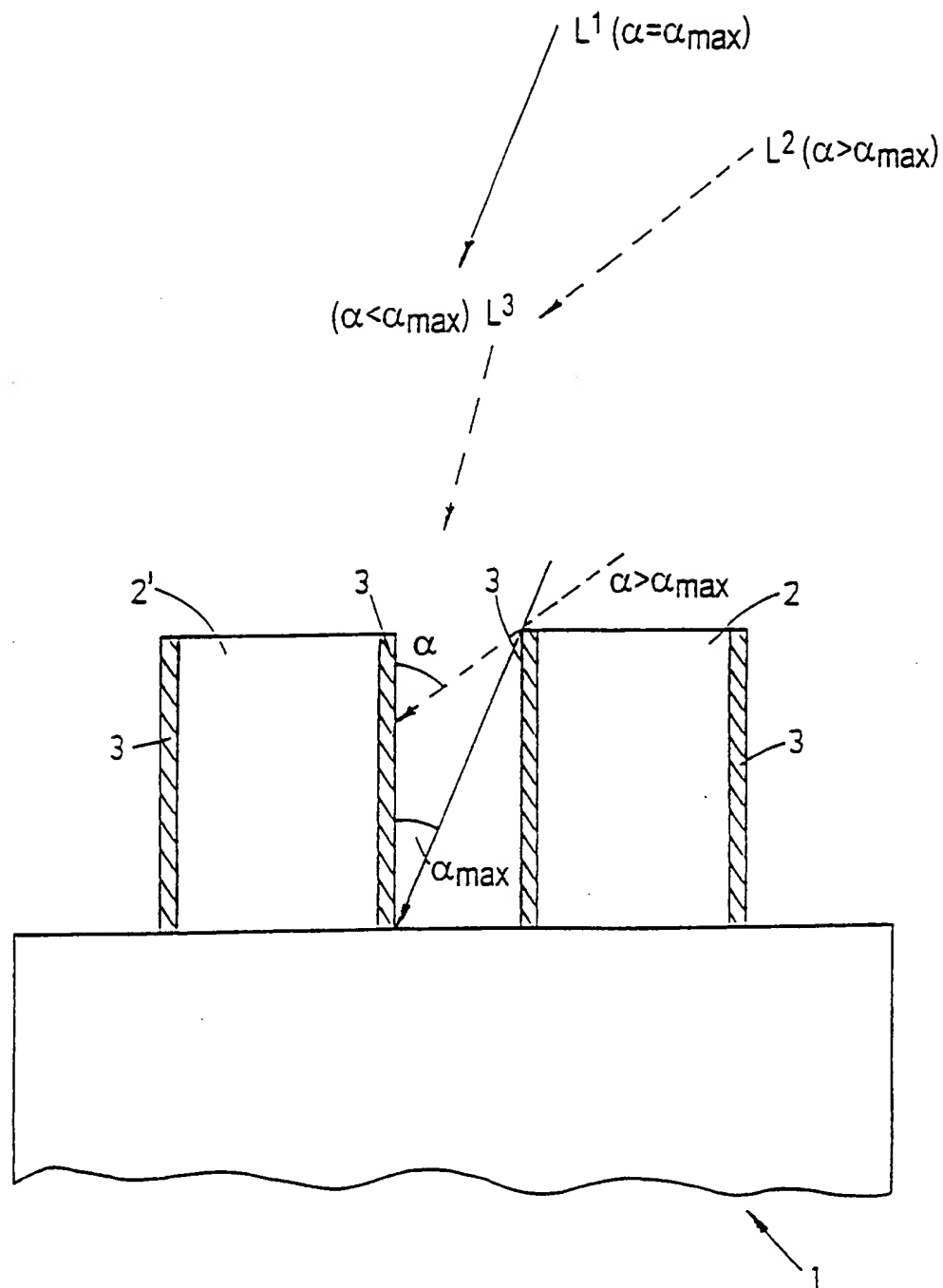


Fig. 1

2/8

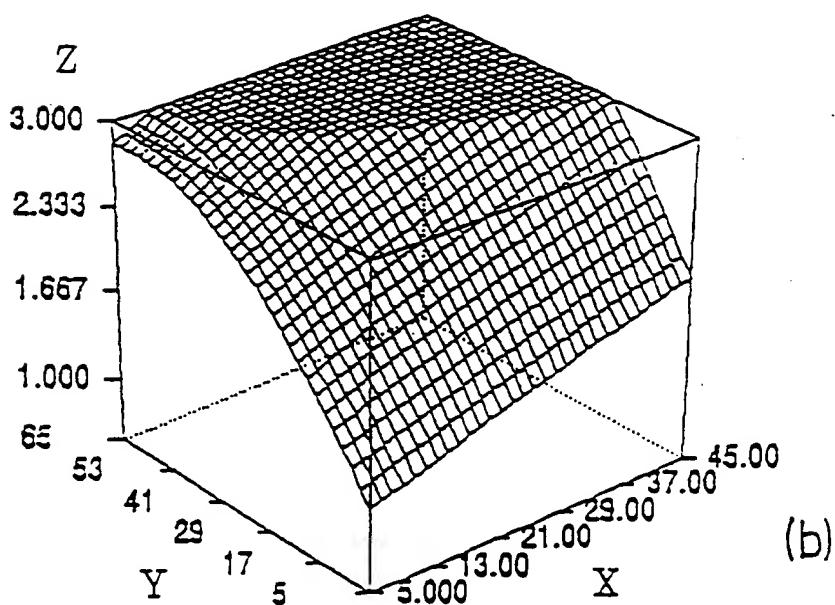
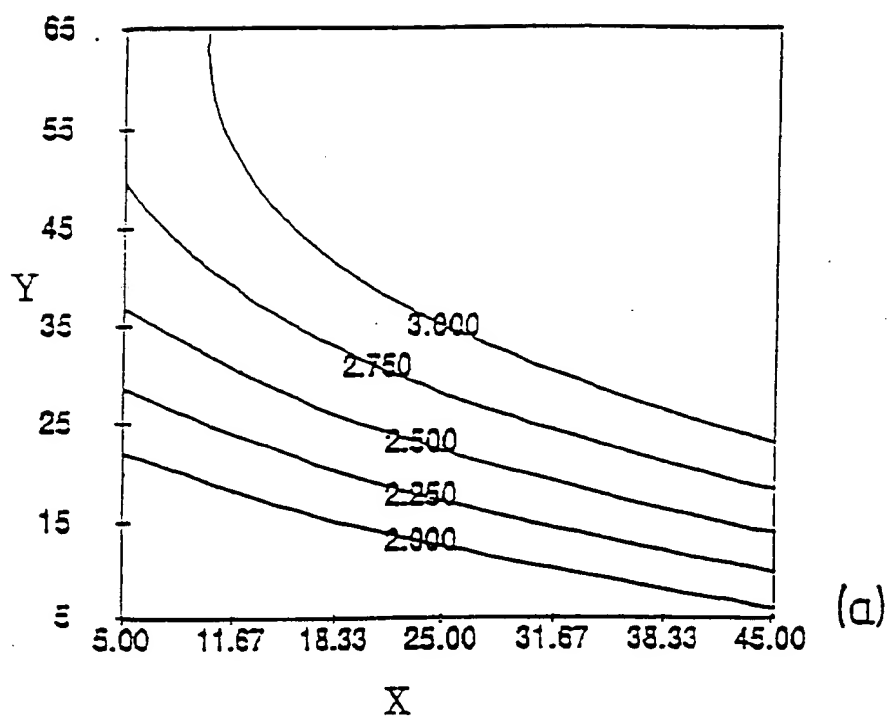


Fig. 2

3/8

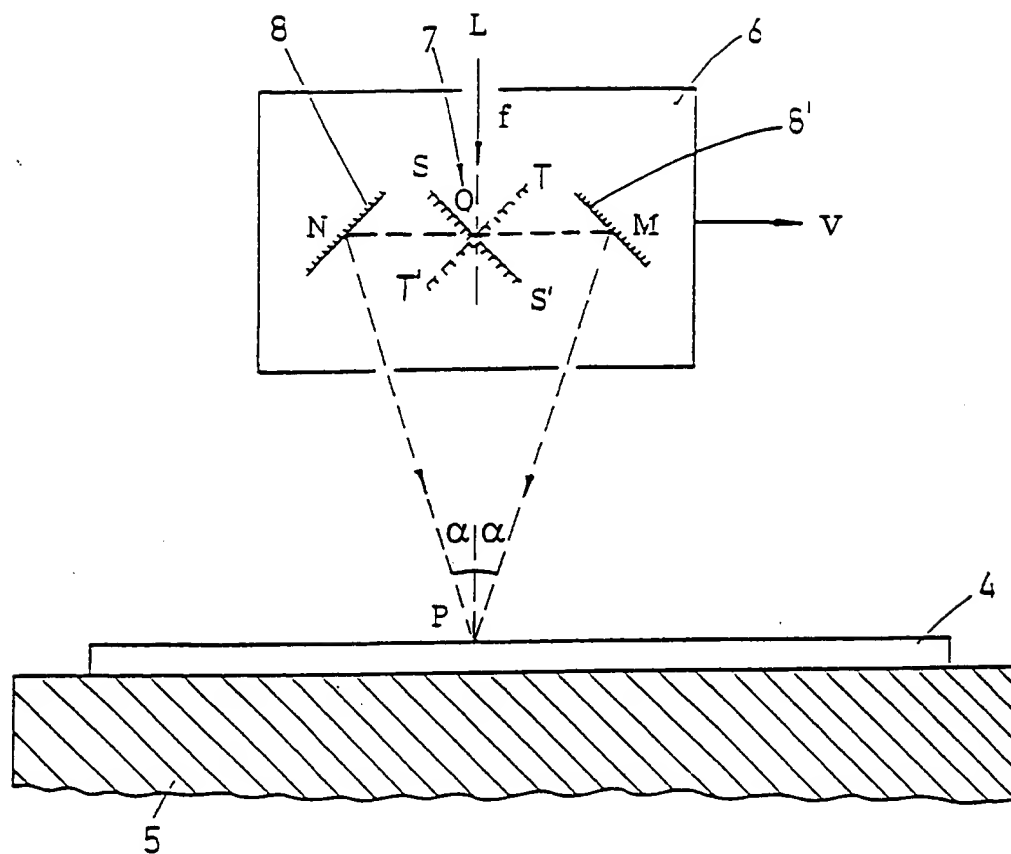


Fig. 3

4/8

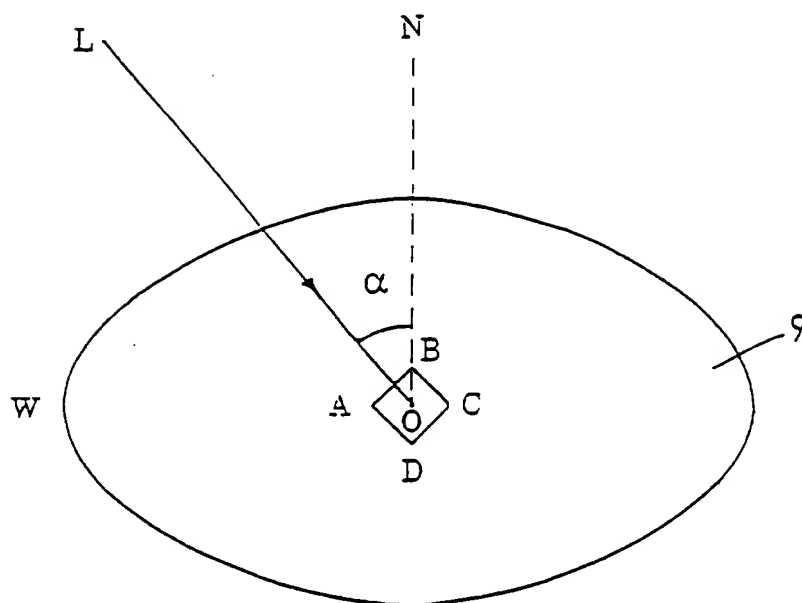


Fig. 4(a)

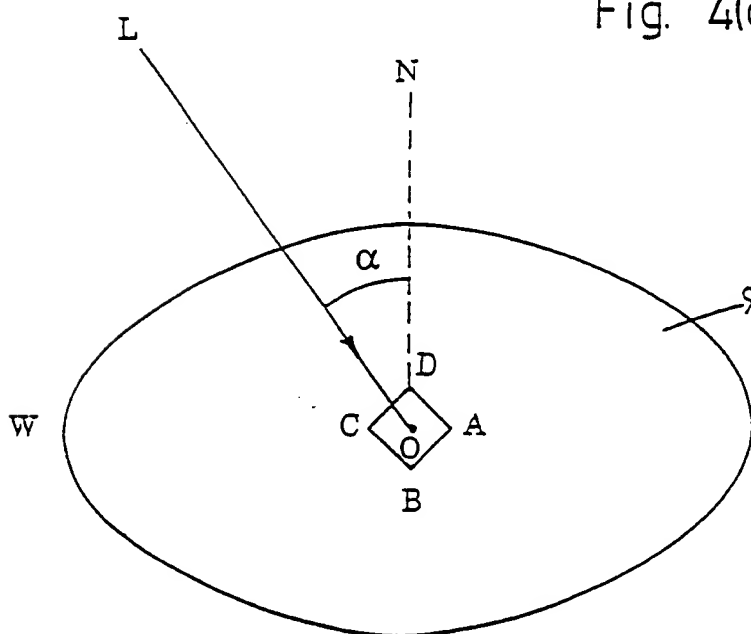


Fig. 4(b)

5/8

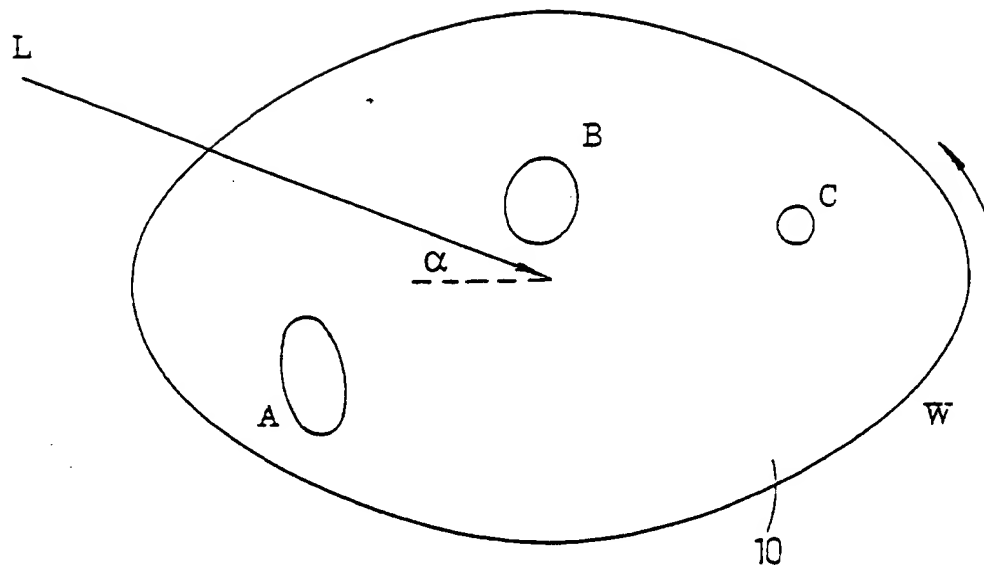


Fig. 5

6/8

1 →

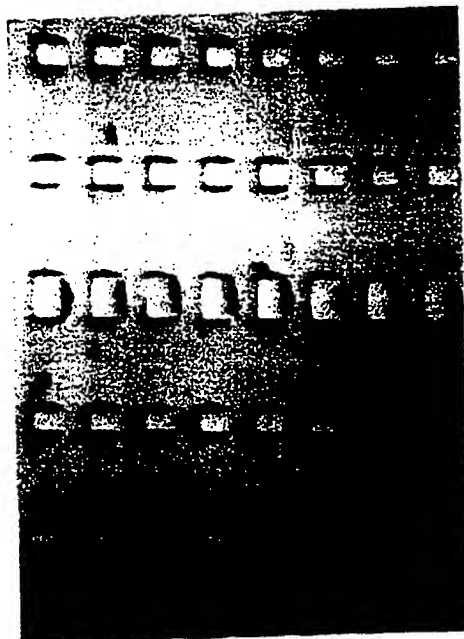


Fig. 6a

2 →

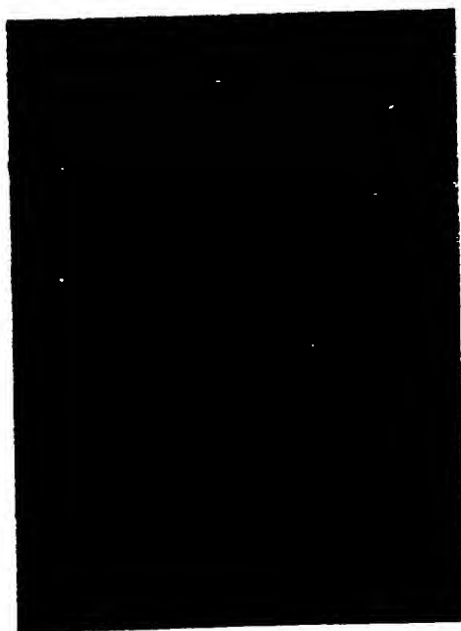


Fig. 6c

7/8

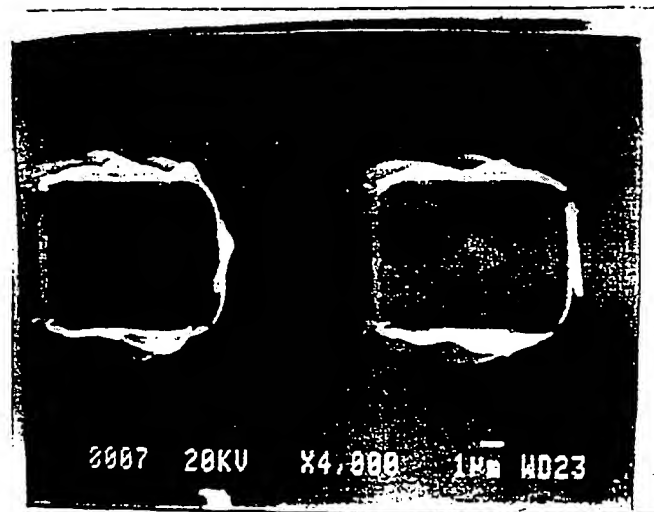


Fig. 6b

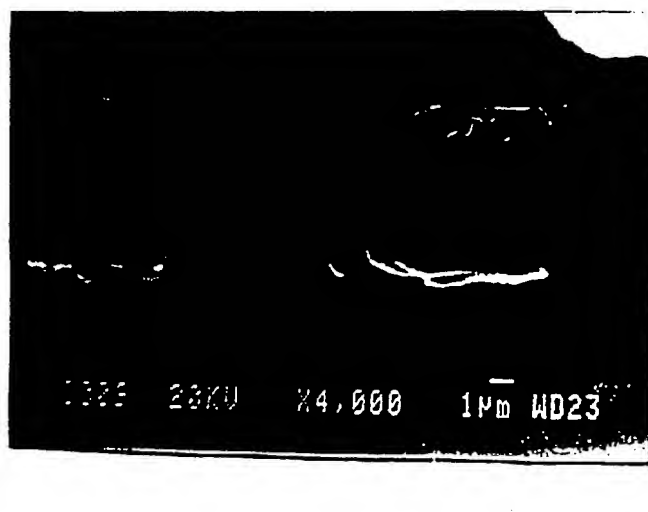


Fig. 6d

8/8



Fig. 6e

INTERNATIONAL SEARCH REPORT

International Application No
PCT/IL 96/00140

A. CLASSIFICATION OF SUBJECT MATTER
IPC 6 B23K26/00 G03F7/42

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)
IPC 6 B23K G03F H01L B29D B08B H01S

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	US 4 914 270 A (COPLY STEPHEN M ET AL) 3 April 1990 see column 4, line 51 - line 64 see column 7, line 35 - column 8, line 63 ---	1,2,4-6, 8-14
X	US 5 023 424 A (VAUGHT JOHN L) 11 June 1991 see column 3, line 45 - line 54 see column 4, line 18 - line 27 ---	1,2,8-14
X	EP 0 407 969 A (MAHO AG) 16 January 1991 see the whole document ---	1,5,8-14
X	EP 0 027 707 A (METAL BOX PLC) 29 April 1981 see page 10, paragraph 2 see page 13, last paragraph ---	1,5, 8-11,13, 14
-/-		

☒ Further documents are listed in the continuation of box C.

☒ Patent family members are listed in annex.

* Special categories of cited documents:

- "A" document defining the general state of the art which is not considered to be of particular relevance
- "E" earlier document but published on or after the international filing date
- "L" document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified)
- "O" document referring to an oral disclosure, use, exhibition or other means
- "P" document published prior to the international filing date but later than the priority date claimed

- "T" later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention
- "X" document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone
- "Y" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art
- "&" document member of the same patent family

Date of the actual completion of the international search

12 February 1997

Date of mailing of the international search report

26. 02. 97

Name and mailing address of the ISA

European Patent Office, P.B. 5818 Patentlaan 2
NL - 2280 HV Rijswijk
Tel. (+31-70) 340-2040, Tx. 31 651 epo nl,
Fax (+31-70) 340-3016

Authorized officer

Aran, D

INTERNATIONAL SEARCH REPORT

Intern. Application No
PCT/IL 96/00140

C.(Continuation) DOCUMENTS CONSIDERED TO BE RELEVANT		
Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A	<p>US 5 114 834 A (NACHSHON YEHUDA) 19 May 1992. cited in the application see the whole document -----</p>	<p>1,2,8, 13,14</p>

INTERNATIONAL SEARCH REPORT

International Application No

PCT/IL 96/00140

Patent document cited in search report	Publication date	Patent family member(s)	Publication date
US-A-4914270	03-04-90	NONE	
US-A-5023424	11-06-91	JP-A- 4211127	03-08-92
EP-A-0407969	16-01-91	DE-C- 3923356	07-02-91
		DE-U- 8911733	16-11-89
		CN-A- 1048996	06-02-91
		DE-D- 59002985	11-11-93
		ES-T- 2054166	01-08-94
		JP-A- 3114688	15-05-91
		US-A- 5149937	22-09-92
		US-A- 5225650	06-07-93
EP-A-0027707	29-04-81	GB-A- 2060455	07-05-81
US-A-5114834	19-05-92	DE-A- 3835636	03-05-89
		FR-A- 2622352	28-04-89
		FR-A- 2622134	28-04-89
		GB-A,B 2211629	05-07-89
		NL-A- 8802587	16-05-89